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843.43311X00

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ANTONELLI, TERRY STOUT & KRAUS, LLP  
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SUITE 1800  
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10/04/2006 RNEBRAH1 00000044 10726507

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Ricardo E. Perez (Depositor's name)  
*Ricardo Perez* (Signature)  
10/03/2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,507	12/04/2003	Digh Hisamoto	843.43311X00	7172

TITLE OF INVENTION: FABRICATION METHOD AND STRUCTURE OF SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE (\$ DUE)	DATE DUE
Nonprovisional	NO	\$1400	\$300	\$1700	10/03/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
FENTY, JESSE A	2815	257-407000

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(A) NAME OF ASSIGNEE

RENESAS TECHNOLOGY CORP.

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

TOKYO, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Date: OCTOBER 3, 2006

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Larry N. Anagnos

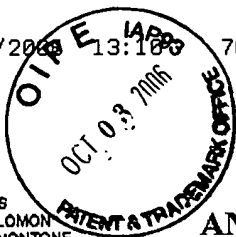
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**FACSIMILE TRANSMITTAL SHEET**  
**OCTOBER 3, 2006**

TO: Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RE: U.S. Patent Application No. **10/726,507**  
For: **FABRICATION METHOD AND STRUCTURE OF SEMICONDUCTOR  
NON-VOLATILE MEMORY DEVICE**  
Filing Date: 12/04/2003  
Atty. Docket No. **843.43311X00**

From: Larry N. Anagnos

Fax Number: (571) 273-2885

Total Number of Pages: **3**

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